

009001" 28248960

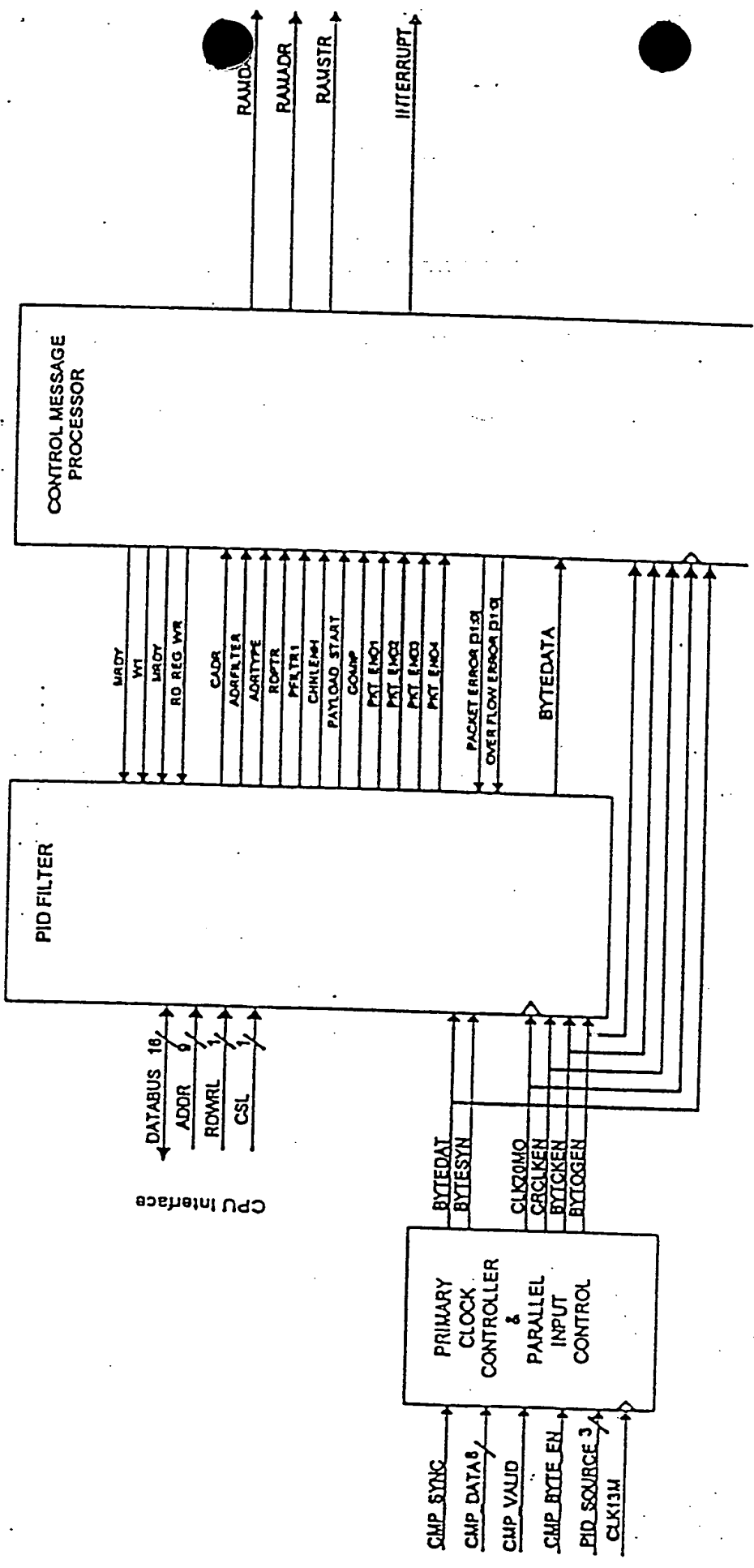


Figure 1A

00900T 28248960

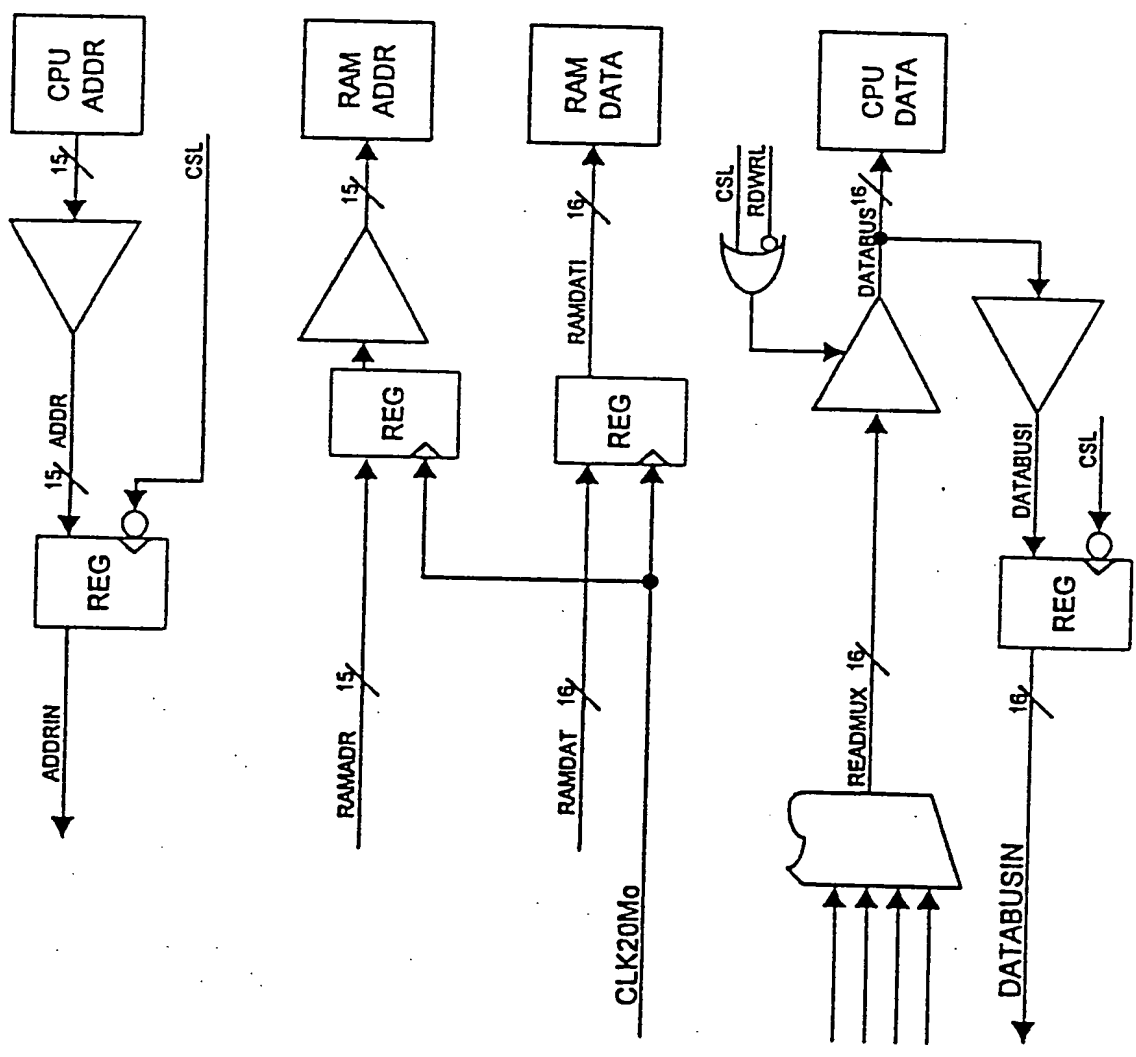


Figure 1B

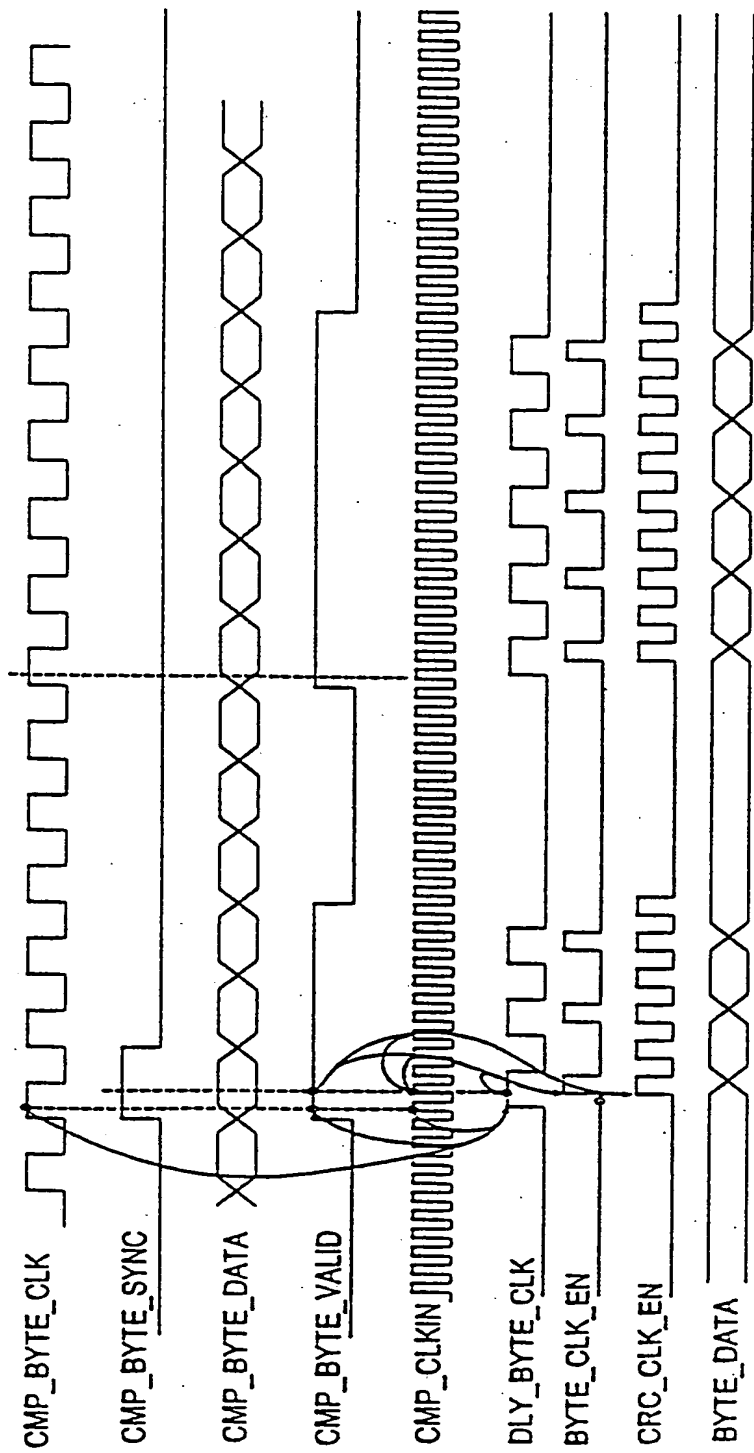


Figure 2

00900T" 48248960

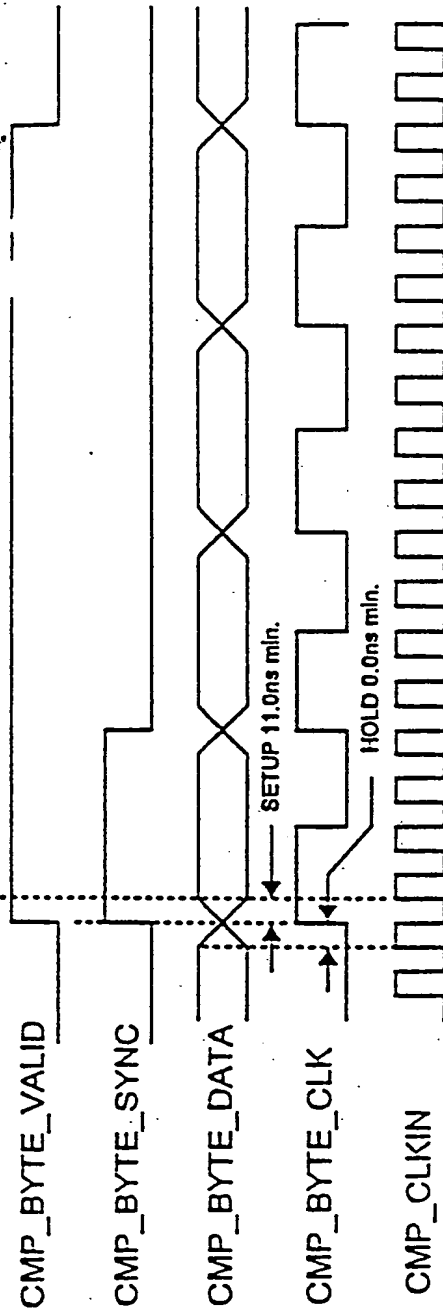


Figure 3

005001/8248960

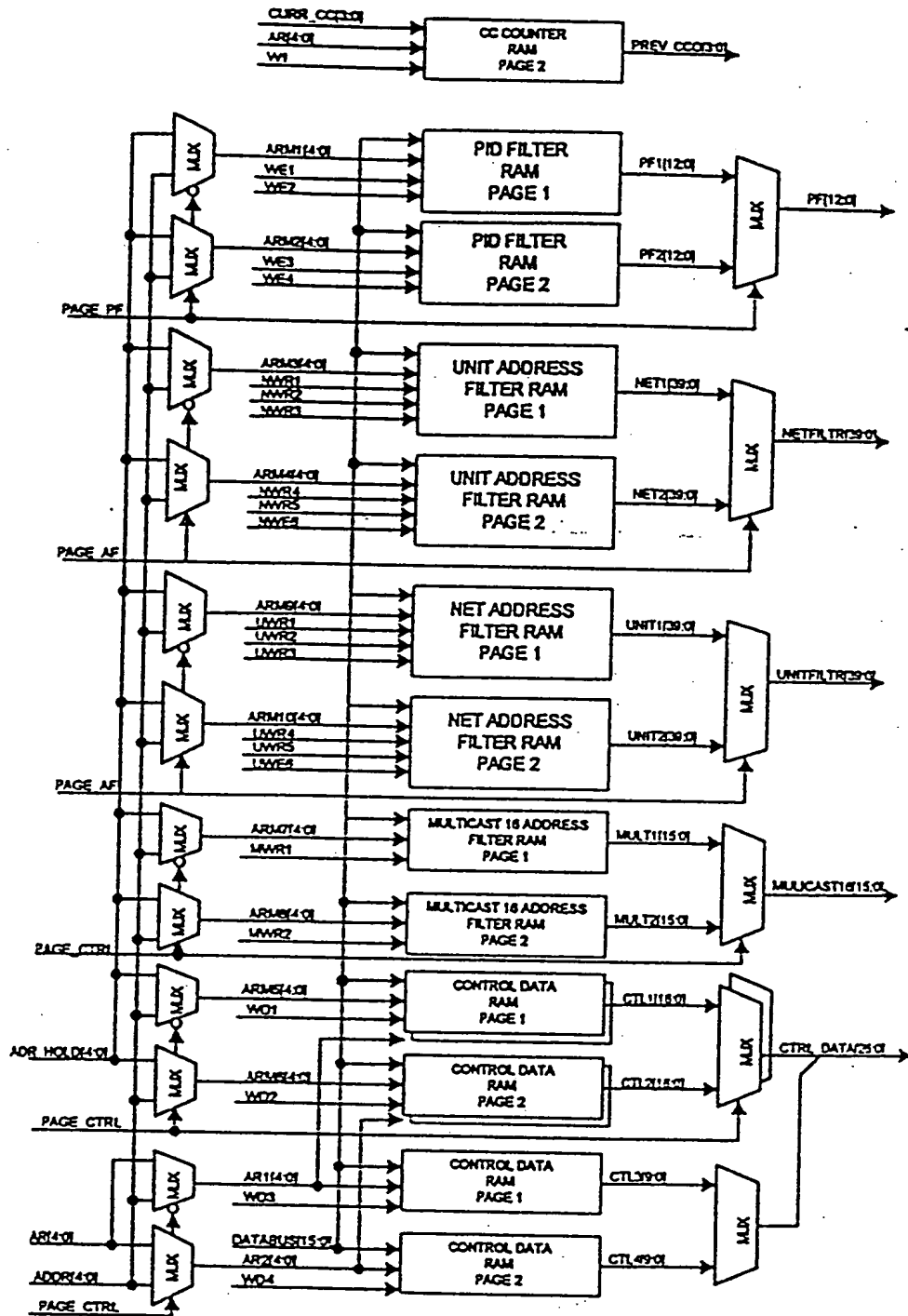


Figure 4A

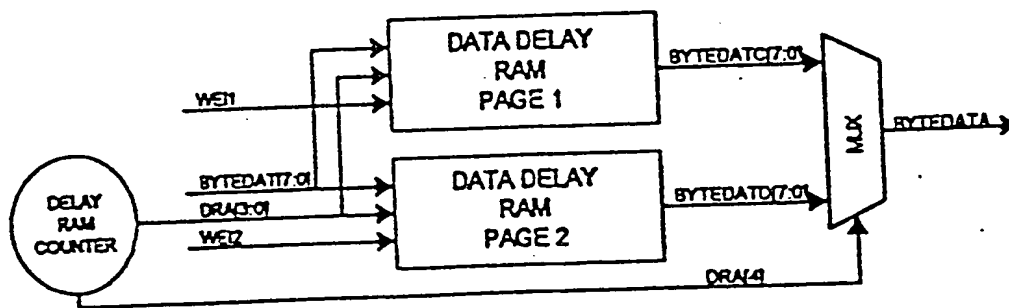
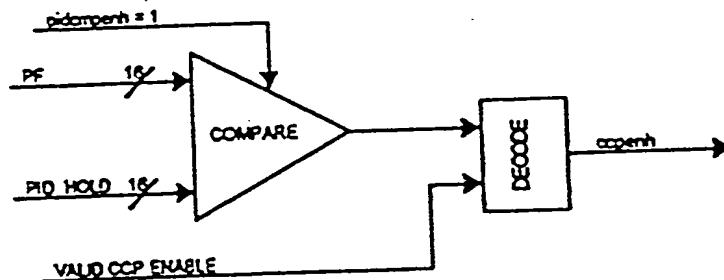
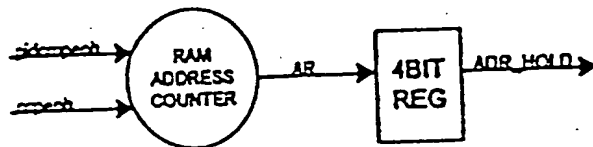
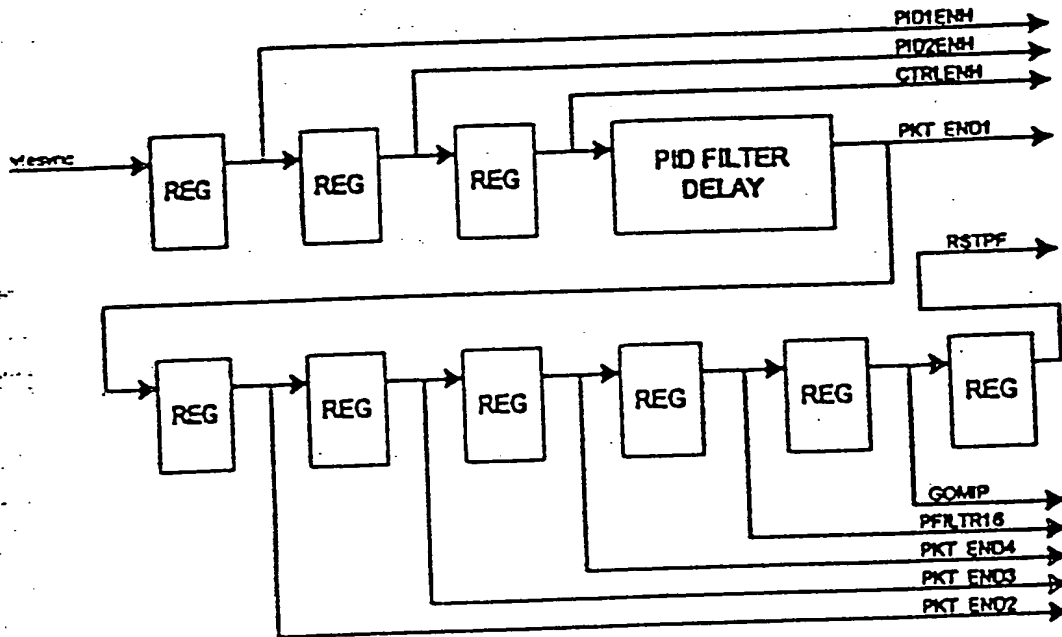
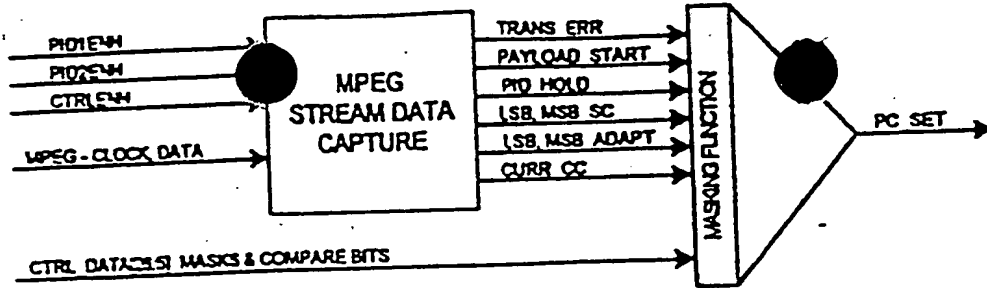


Figure 4b

00900128248360

00900T 28 24 2960

MPEG HEADER

PACKET PROCESSING
TYPE 1

CRCs ARE STORED FOR COMPLETE MESSAGES, STATE MACHINE IS RESET, & DATA VARIABLES ARE SAVED TO RAM

END OF PREVIOUS PACKET
START OF NEW PACKET

GO MIP

PKT END 1

PACKET PROCESSING
TYPE 2

PUSH AHEAD ONE BYTE

CRCs ARE STORED FOR COMPLETE MESSAGES, STATE MACHINE IS RESET, & DATA VARIABLES ARE SAVED TO RAM

GO MIP

PKT END 2

PACKET PROCESSING
TYPE 3

PUSH AHEAD TWO BYTES

INSERT BYTE LEFT FROM PREVIOUS PACKET

CRCs ARE STORED FOR COMPLETE MESSAGES, STATE MACHINE IS RESET, & DATA VARIABLES ARE SAVED TO RAM

GO MIP

PKT END 3

PACKET PROCESSING
TYPE 4

PUSH AHEAD ONE BYTE

INSERT BYTE LEFT FROM PREVIOUS PACKET

CRCs ARE STORED FOR COMPLETE MESSAGES, STATE MACHINE IS RESET, & DATA VARIABLES ARE SAVED TO RAM

GO MIP

PKT END 2

PKT END 4

Figure 5

09684287 100000

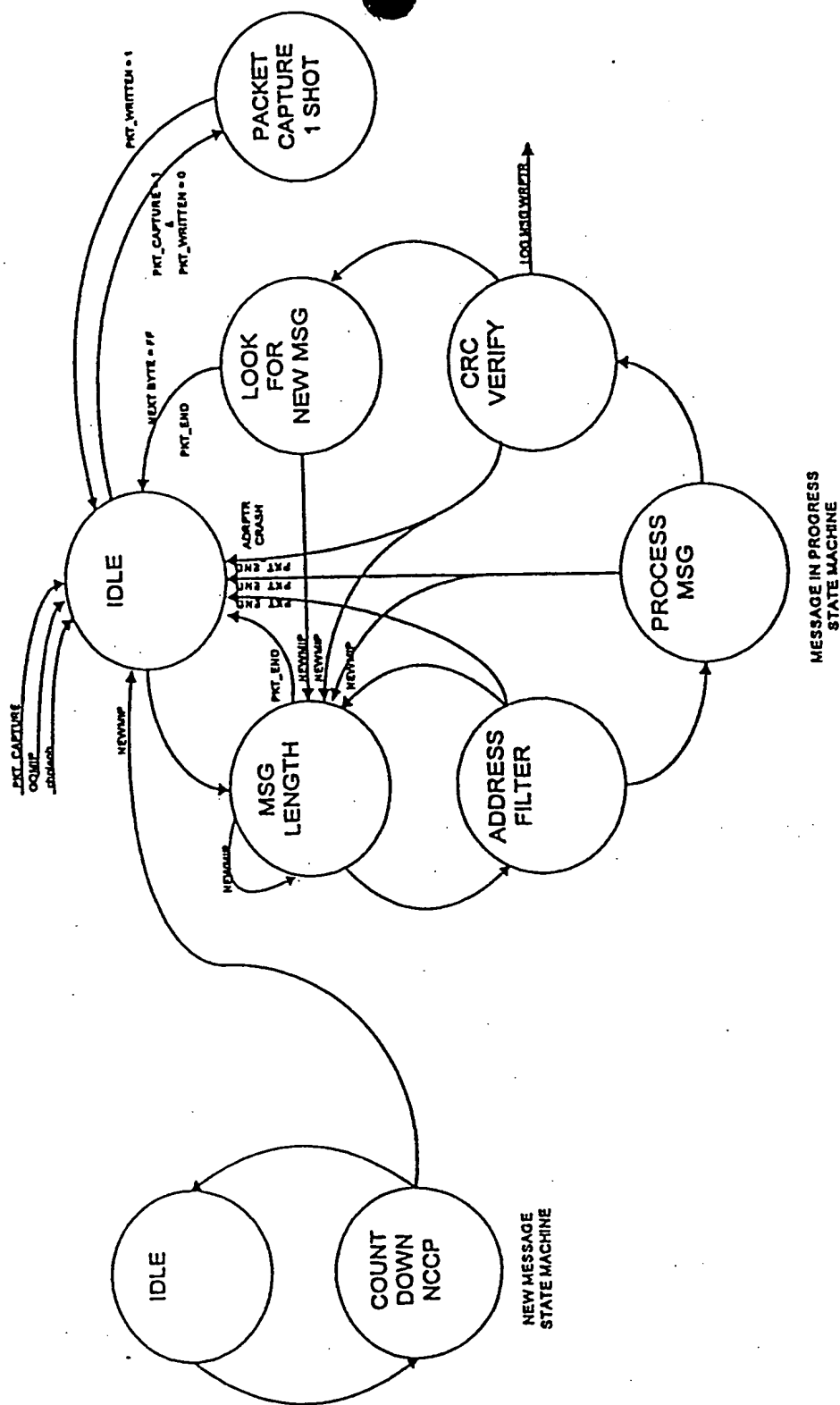


Figure 6

All RAMs are synchronous to the system clock

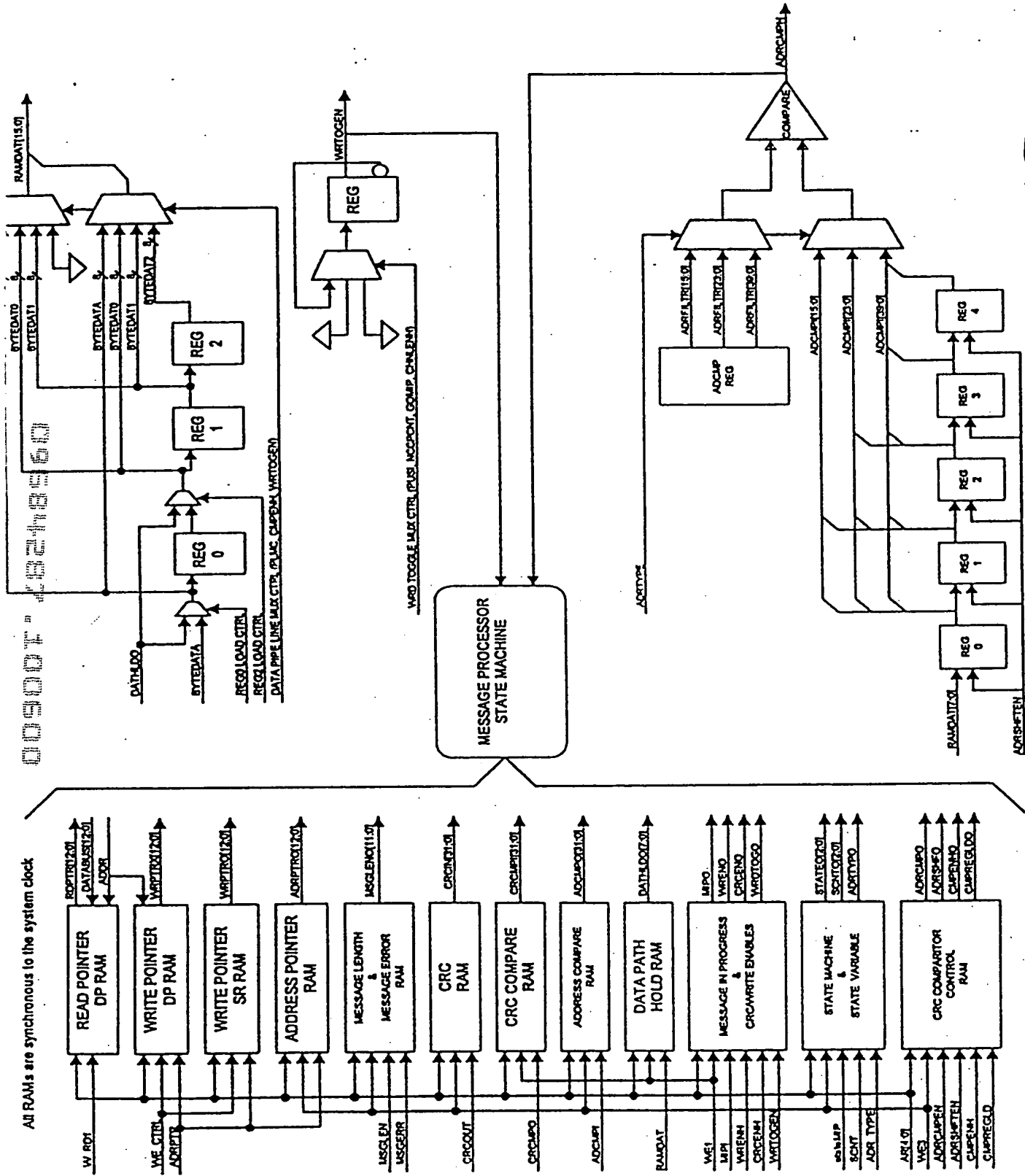


Figure 7

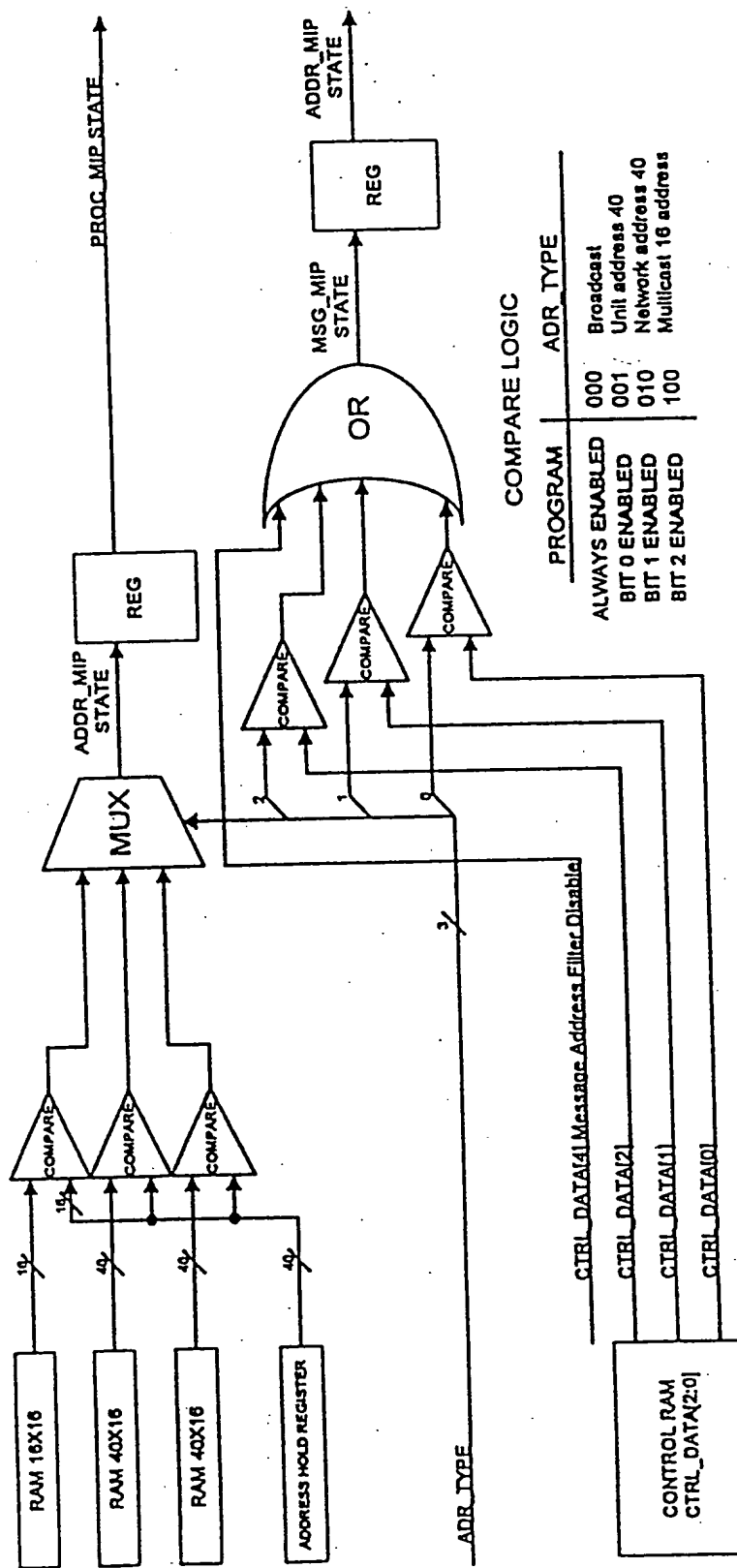


Figure 8

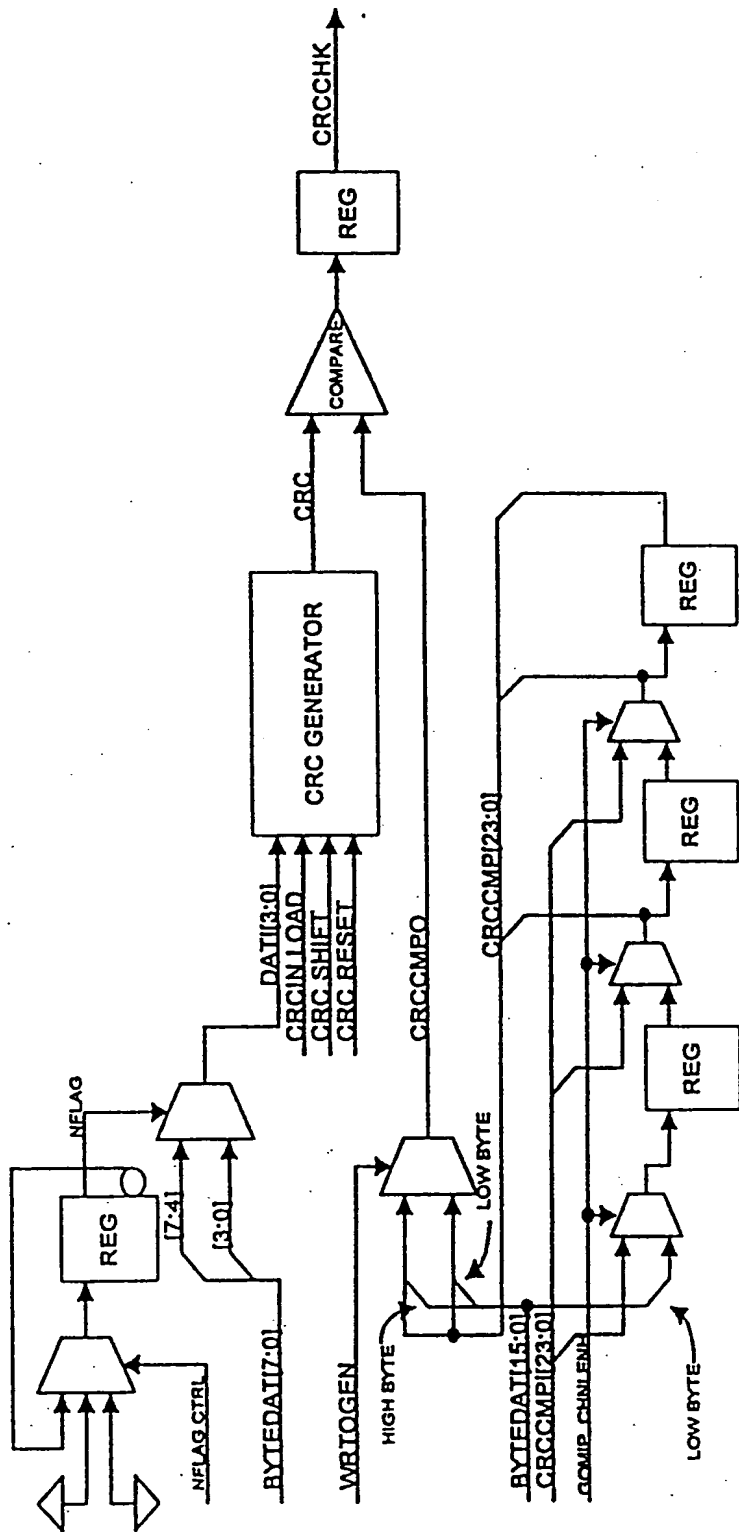


Figure 9

00900T 48248950

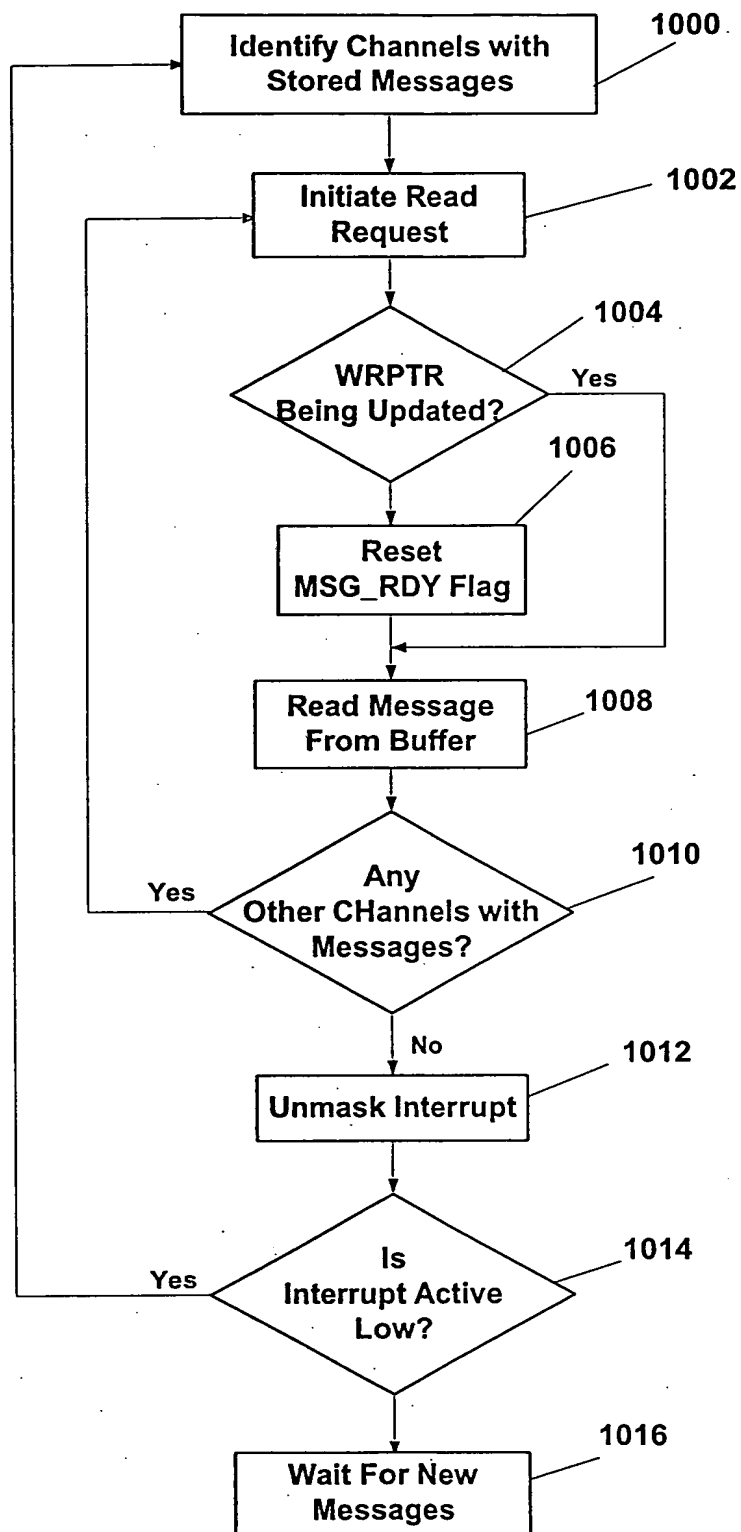


Fig. 10

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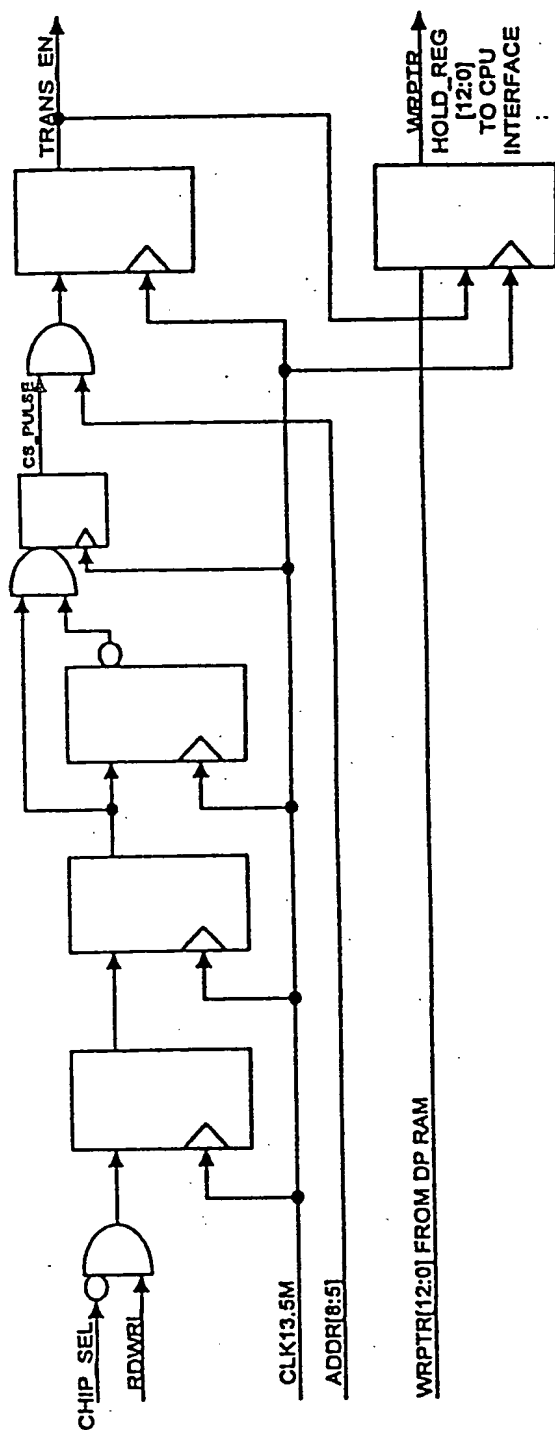


Figure 11

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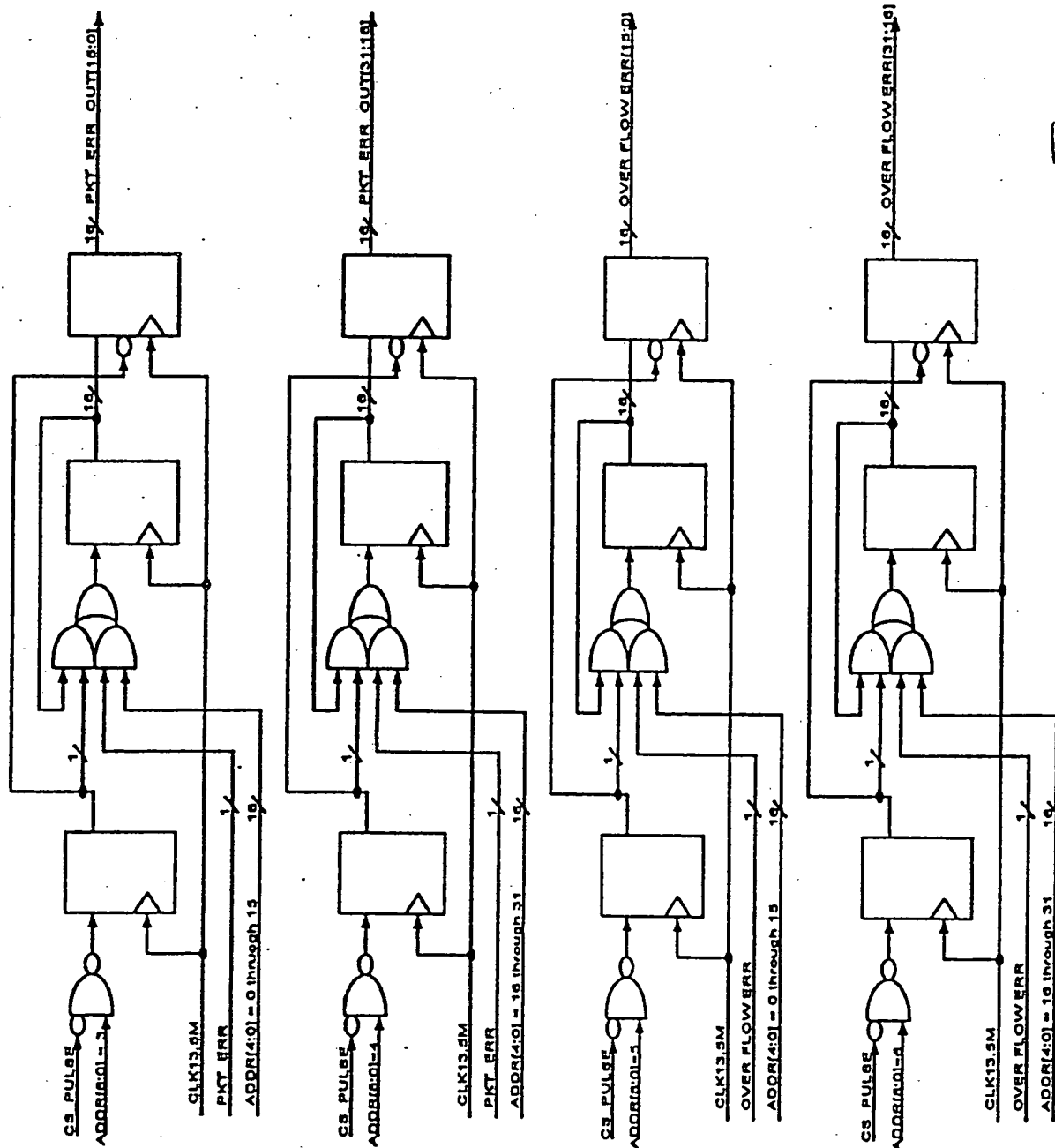


Figure 12